

Notice of References Cited

Application/Control No.

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Applicant(s)/Patent Under

Reexamination

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Examiner

AFSAR M. QURESHI

Art Unit

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,467,083	10-2002	Yamashita, Fumiaki	717/128
	B	US-			
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	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	THE NEXUS 5001 FORUM: "Standard for a Global Embedded Processor Debug Interface", 12/15/1999, IEEE-ISTO, XP002247195
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	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a))
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